

Job Title: NPI Design Engineer	Location: Hunmanby
Department: Engineering	Contract: Permanent/Full-time
Reports To: Hardware Team Manager	Direct Reports: None

# 1.0 Job Summary & Role

The individual will work within the Engineering department located at DSE headquarters in Hunmanby (North Yorkshire). They will work on a range of tasks introducing designs from internal and external designs into our production facility. They will also contribute to design tasks on new products as a member of the hardware design team.

# 2.0 Key Responsibilities & Main Duties

- Facilitate the seamless transition of external designs to the in-house production facility at DSE.
- Review designs for production suitability and work with external entities to make improvements
- Follow a defined process and maintain a consistent approach in ERP and eCAD systems.
- Gain a thorough understanding of manufacturing processes and review designs for production readiness and suitability for high-volume builds with increasing levels of automation.
- Work independently on new and existing designs to integrate cost-saving opportunities into analogue, digital, and microcontroller circuits for controls and automation applications.
- Estimate individual tasks and work packages, contributing to project planning activities and identifying and mitigating areas of risk.
- Maintain high levels of attention to detail and perform administrative activities involved in creating and maintaining BOMs in the company ERP system.
- Contribute to the training and coaching of less experienced and technical colleagues, assisting them in achieving their goals.
- Work with team managers and leaders to progress multiple projects in parallel.

#### 1.0 Internal & External Relationships

• Engineering Management – take work tasks and direction from and report to





- Test and Approvals department work closely with the test and approvals team to ensure correct level of testing.
- Work with purchasing department to qualify new parts
- Coordinating with other departments to ensure smooth production of final products

### 2.0 Key Performance Indicators

- Attention to detail, able to work both individually as a part of a team
- Efficient approach to tasks.
- Ability to work against timescales and deliver the test reports with the expected quality on time.

# 3.0 Essential/Desirable Factors

Knowledge		
<ul> <li>Know</li> <li>Essential: <ul> <li>Basic electronic component and circuit theory</li> <li>Analogue and digital Hardware design</li> <li>EMC Principles</li> <li>Familiar with stage gated / agile</li> </ul> </li> </ul>	/ledge Desirable: - Electrical theory for power generation	
<ul> <li>development approaches.</li> <li>ERP systems with MRP modules</li> <li>Good understanding of SMT, PTH components and PCB assembly process</li> </ul>		

#### **Skills & Attributes**

Essential:	Desirable:
- Attention to detail	<ul> <li>Ability to bring in technical data from external companies and prepare for use</li> </ul>
<ul> <li>Used to working to a high-quality standard.</li> </ul>	in DSE manufacturing process.
<ul> <li>Excellent team player with problem solving and trouble-shooting capabilities</li> </ul>	
<ul> <li>Used to defining a task breakdowns for</li> </ul>	
a give piece of work and providing	
estimates.	
<ul> <li>Ability to translate requirements into a technical product specification</li> </ul>	
<ul> <li>Used to working in a high-paced environment</li> </ul>	
- Good communication skills and ability to	
discuss hardware design issues within	
wider audiences	
- Enthusiastic and optimistic.	

Experience		
<ul> <li>Essential:</li> <li>ERP systems and BOM structures</li> <li>Ability to read and translate Schematic capture S/W</li> <li>Experience with Altium</li> </ul>	Desirable: - Knowledge of Cadstar - Epicor ERP system	



# JOB DESCRIPTION & PERSON SPECIFICATION

DSE

- Creating component acceptability reports

Qualifications			
Essential:	Desirable:		
<ul> <li>HNC in Electrical Engineering or equivalent</li> </ul>	<ul> <li>HND or BEng in Electrical Engineering or equivalent</li> </ul>		

Created by	Dated Created
Engineering Management	18/09/2024

